



CVE-2021-1104

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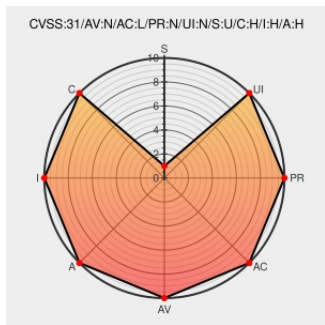
Last Modified on: 08/23/2021 08:30:00 PM UTC

CVE-2021-1104

Source: Mitre

Source: Nist

Print: PDF



Certain versions of [Instruction Set Manual](#) from [Risc-v](#) contain the following vulnerability:

The RISC-V Instruction Set Manual contains a documented ambiguity for the Machine Trap Vector Base Address (MTVEC) register that may lead to a vulnerability due to the initial state of the register not being defined, potentially leading to information disclosure, data tampering and denial of service.

CVE-2021-1104 has been assigned by psirt@nvidia.com to track the vulnerability - currently rated as **CRITICAL** severity.

Affected Vendor/Software: **RISCV.org - Machine Trap Base Address (MTVEC) Register** version **All versions**

CVSS3 Score: **9.8 - CRITICAL**

Attack Vector	Attack Complexity	Privileges Required	User Interaction
NETWORK	LOW	NONE	NONE
Scope	Confidentiality Impact	Integrity Impact	Availability Impact
UNCHANGED	HIGH	HIGH	HIGH

CVSS2 Score: **7.5 - HIGH**

Access Vector	Access Complexity	Authentication
NETWORK	LOW	NONE
Confidentiality Impact	Integrity Impact	Availability Impact
PARTIAL	PARTIAL	PARTIAL

CVE References

Description	Tags	Link
Video: Glitching RISC-V chips: MTVEC corruption for hardening		CONFIRM riscv.org/news/2021/08/video-glitching-risc-v-

By selecting these links, you may be leaving CVEreport webspace. We have provided these links to other websites because they may have information that would be of interest to you. No inferences should be drawn on account of other sites being referenced, or not, from this page. There may be other websites that are more appropriate for your purpose. CVEreport does not necessarily endorse the views expressed, or concur with the facts presented on these sites. Further, CVEreport does not endorse any commercial products that may be mentioned on these sites. Please address comments about any linked pages to comment@cve.report.

There are currently no QIDs associated with this CVE

Known Affected Configurations (CPE V2.3)

Type	Vendor	Product	Version	Update	Edition	Language
Application	Risc-v	Instruction Set Manual	All	All	All	All
cpe:2.3:a:risc-v:instruction_set_manual:*:*:*:*:*:						

No vendor comments have been submitted for this CVE

Social Mentions

Source	Title	Posted (UTC)
 @CVEreport	CVE-2021-1104 : The RISC-V Instruction Set Manual contains a documented ambiguity for the Machine Trap Vector Base... twitter.com/i/web/status/1...	2021-08-13 16:05:21

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