



CVE-2021-26409

[MITRE](#)[NVD](#)[CVE.ORG](#)[Print: PDF](#)

Summary

| | |
|------------------------|---|
| CVE | CVE-2021-26409 |
| State | PUBLIC |
| Assigner | psirt@amd.com |
| Source Priority | CVE Program / NVD first with legacy fallback |
| Published | 2023-01-11 08:15:00 UTC |
| Updated | 2023-11-07 03:31:00 UTC |
| Description | Insufficient bounds checking in SEV-ES may allow an attacker to corrupt Reverse Map table (RMP) memory, potentially res |

Risk And Classification

Problem Types: CWE-120

NVD Known Affected Configurations (CPE 2.3)

| Type | Vendor | Product | Version | Update | Edition | Language |
|------------------|---------------------|----------------------------------|---------|--------|---------|----------|
| Hardware | Amd | Milanpi | - | All | All | All |
| Operating System | Amd | Milanpi Firmware | All | All | All | All |

References

| Reference | Source | Link | Tags |
|--|---------|--|---------------------|
| www.amd.com/en/corporate/product-security/bulletin/AMD-SB-1032 | MISC | www.amd.com | |
| CVE Program record | CVE.ORG | www.cve.org | canonical |
| NVD vulnerability detail | NVD | nvd.nist.gov | canonical, analysis |

No vendor comments have been submitted for this CVE.

There are currently no legacy QID mappings associated with this CVE.

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