



# CVE-2021-33147

[MITRE](#)[NVD](#)[CVE.ORG](#)[Print: PDF](#)

## Summary

<b>CVE</b>	CVE-2021-33147
<b>State</b>	PUBLIC
<b>Assigner</b>	secure@intel.com
<b>Source Priority</b>	CVE Program / NVD first with legacy fallback
<b>Published</b>	2022-02-09 23:15:00 UTC
<b>Updated</b>	2022-02-15 16:51:00 UTC
<b>Description</b>	Improper conditions check in the Intel(R) IPP Crypto library before version 2021.2 may allow an authenticated user to poter

## Risk And Classification

**Problem Types:** CWE-754

## NVD Known Affected Configurations (CPE 2.3)

Type	Vendor	Product	Version	Update	Edition	Language
Application	Intel	Integrated Performance Primitives Cryptography	All	All	All	All

## References

Reference	Source	Link	Tags
INTEL-SA-00600	MISC	<a href="http://www.intel.com">www.intel.com</a>	
CVE Program record	CVE.ORG	<a href="http://www.cve.org">www.cve.org</a>	canonical
NVD vulnerability detail	NVD	<a href="http://nvd.nist.gov">nvd.nist.gov</a>	canonical, analysis

No vendor comments have been submitted for this CVE.

There are currently no legacy QID mappings associated with this CVE.

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