



# CVE-2021-40507

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## Summary

<b>CVE</b>	CVE-2021-40507
<b>State</b>	PUBLIC
<b>Assigner</b>	cve@mitre.org
<b>Source Priority</b>	CVE Program / NVD first with legacy fallback
<b>Published</b>	2023-04-18 12:15:00 UTC
<b>Updated</b>	2023-04-27 15:41:00 UTC
<b>Description</b>	An issue was discovered in the ALU unit of the OR1200 (aka OpenRISC 1200) processor 2011-09-10 through 2015-11-11.

## Risk And Classification

**Problem Types:** CWE-287

## NVD Known Affected Configurations (CPE 2.3)

Type	Vendor	Product	Version	Update	Edition	Language
Hardware	<a href="#">Openrisc</a>	<a href="#">Or1200</a>	-	All	All	All
Operating System	<a href="#">Openrisc</a>	<a href="#">Or1200 Firmware</a>	All	All	All	All

## References

Reference	Source	Link	Tags
New vulnerabilities detected by our hardware fuzzer, TheHuzz	MISC	<a href="http://seth.engr.tamu.edu">seth.engr.tamu.edu</a>	
or1200: add carry, overflow bits, and range exception · openrisc/or1200@2c0765d · GitHub	MISC	<a href="https://github.com">github.com</a>	
CVE Program record	CVE.ORG	<a href="http://www.cve.org">www.cve.org</a>	canonical
NVD vulnerability detail	NVD	<a href="http://nvd.nist.gov">nvd.nist.gov</a>	canonical, a

No vendor comments have been submitted for this CVE.

There are currently no legacy QID mappings associated with this CVE.

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