



# CVE-2021-41612

[MITRE](#)[NVD](#)[CVE.ORG](#)[Print: PDF](#)

## Summary

<b>CVE</b>	CVE-2021-41612
<b>State</b>	PUBLIC
<b>Assigner</b>	cve@mitre.org
<b>Source Priority</b>	CVE Program / NVD first with legacy fallback
<b>Published</b>	2023-04-18 12:15:00 UTC
<b>Updated</b>	2023-04-27 19:46:00 UTC
<b>Description</b>	An issue was discovered in the ALU unit of the OpenRISC mor1kx processor. The carry flag is not being updated correctly

## Risk And Classification

**Problem Types:** NVD-CWE-noinfo

## NVD Known Affected Configurations (CPE 2.3)

Type	Vendor	Product	Version	Update	Edition	Language
Hardware	<a href="#">Openrisc</a>	<a href="#">Mor1kx</a>	-	All	All	All
Operating System	<a href="#">Openrisc</a>	<a href="#">Mor1kx Firmware</a>	All	All	All	All

## References

Reference	Source	Link
New vulnerabilities detected by our hardware fuzzer, TheHuzz	MISC	<a href="#">seth.engr.tamu.ec</a>
The carry flag implementation is not correct for subtract instructions · Issue #139 · openrisc/mor1kx · GitHub	MISC	<a href="#">github.com</a>
CVE Program record	CVE.ORG	<a href="#">www.cve.org</a>
NVD vulnerability detail	NVD	<a href="#">nvd.nist.gov</a>

No vendor comments have been submitted for this CVE.

There are currently no legacy QID mappings associated with this CVE.

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**CVE.report and Source URL Uptime Status [status.cve.report](#)**