

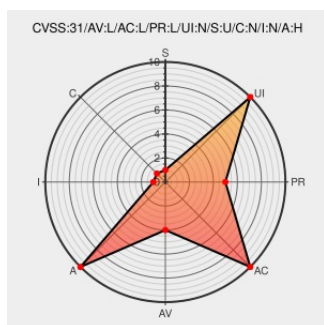


CVE-2022-34643

Published on: Not Yet Published

Last Modified on: 07/26/2022 06:11:00 PM UTC

CVE-2022-34643

[Source: Mitre](#)[Source: NIST](#)[CVE.ORG](#)[Print: PDF](#)

Certain versions of [Spike Risc-v Isa Simulator](#) from [Riscv](#) contain the following vulnerability:

RISCV ISA Sim commit

ac466a21df442c59962589ba296c702631e041b5 implements the incorrect exception priority when accessing memory.

CVE-2022-34643 has been assigned by [M](#) cve@mitre.org to track the vulnerability - currently rated as **MEDIUM** severity.

CVSS3 Score: **5.5 - MEDIUM**

Attack Vector	Attack Complexity	Privileges Required	User Interaction
LOCAL	LOW	LOW	NONE
Scope	Confidentiality Impact	Integrity Impact	Availability Impact
UNCHANGED	NONE	NONE	HIGH

CVE References

Description	Tags	Link
[Bug Report] Wrong exception priority during access memory · Issue #971 · riscv-software-src/riscv-isa-sim · GitHub	github.com text/html	MISC github.com/riscv-software-src/riscv-isa-sim/issues/971

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There are currently no QIDs associated with this CVE

Known Affected Configurations (CPE V2.3)

Type	Vendor	Product	Version	Update	Edition	Language
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Application

Riscv

Spike Risc-v Isa Simulator

-

All

All

All

cpe:2.3:a:riscv:spike_risc-v_isa_simulator:-:*:*:*:*:*:

No vendor comments have been submitted for this CVE

Social Mentions

Source	Title	Posted (UTC)
 @CVEreport	CVE-2022-34643 : RISCv ISA Sim commit ac466a21df442c59962589ba296c702631e041b5 implements the incorrect exception p... twitter.com/i/web/status/1...	2022-07-18 23:07:49
 @Inceptus3	New Vulnerability: CVE-2022-34643 #InceptusSecure #UnderOurProtection	2022-07-19 00:15:23
 @threatmeter	CVE-2022-34643 RISCv ISA Sim commit ac466a21df442c59962589ba296c702631e041b5 implements the incorrect exception pri... twitter.com/i/web/status/1...	2022-07-19 07:09:23
 @ColorTokensInc	Emerging Vulnerability Found CVE-2022-34643 - RISCv ISA Sim commit ac466a21df442c59962589ba296c702631e041b5 impleme... twitter.com/i/web/status/1...	2022-07-19 07:09:30
 /r/netcve	CVE-2022-34643	2022-07-18 23:38:58

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