



# CVE-2023-20585

[MITRE](#)[NVD](#)[CVE.ORG](#)[JSON API](#)[Print: PDF](#)

## Summary

<b>CVE</b>	CVE-2023-20585
<b>State</b>	PUBLISHED
<b>Assigner</b>	AMD
<b>Source Priority</b>	CVE Program / NVD first with legacy fallback
<b>Published</b>	2026-04-16 19:16:31 UTC
<b>Updated</b>	2026-04-16 20:16:34 UTC
<b>Description</b>	Insufficient checks of the RMP on host buffer access in IOMMU may allow an attacker with privileges and a compromised h

## Risk And Classification

**Primary CVSS:** v4.0 5.6 MEDIUM from psirt@amd.com

CVSS:4.0/AV:L/AC:H/AT:N/PR:H/UI:N/VC:N/VI:H/VA:N/SC:N/SI:N/SA:N/E:X/CR:X/IR:X/AR:X/MAV:X/MAC:X/MAT:X/MPR:X/MUI:X/MVC:X/MVI:X/MVA:X/MSX/MSI:X/MSA:X/S:X/AU:X/R:X/V:X/RE:X/U:X

**Problem Types:** CWE-788 | CWE-788 CWE-788 Access of Memory Location After End of Buffer

Version	Source	Type	Score	Severity	Vector
4.0	psirt@amd.com	Secondary	5.6	MEDIUM	CVSS:4.0/AV:L/AC:H/AT:N/PR:H/UI:N/VC:N/VI:H/VA:N/SC:N/SI:N/SA:N/E:X/C...
4.0	CNA	CVSS	5.6	MEDIUM	CVSS:4.0/AV:L/AC:H/AT:N/PR:H/UI:N/VC:N/VI:H/VA:N/SC:N/SI:N/SA:N

## CVSS v4.0 Breakdown

Attack Vector

Local

Attack Complexity

High

Attack Requirements

None

Privileges Required

High

User Interaction

None

Confidentiality

None

Integrity

High

Availability

None

Sub Conf.

None

Sub Integrity

None

Sub Availability

None

CVSS:4.0/AV:L/AC:H/AT:N/PR:H/UI:N/VC:N/VI:H/VA:N/SC:N/SI:N/SA:N/E:X/CR:X/IR:X/AR:X/MAV:X/MAC:X/MAT:X/MPR:X/MUI:X/MVC:X/MVI:X/MVA:X/MSX/MSI:X/MSA:X/S:X/AU:X/R:X/V:X/RE:X/U:X

### Vendor Declared Affected Products

Source	Vendor	Product	Version	Platforms
CNA	AMD	AMD EPYC 7003 Series Processors	unaffected SEV FW 1.37.23	Not specified
CNA	AMD	AMD EPYC 9004 Series Processors	unaffected SEV FW 1.37.31	Not specified
CNA	AMD	AMD EPYC Embedded 7003 Series Processors	unaffected EmbMilanPI-SP3 1.0.0.B	Not specified
CNA	AMD	AMD EPYC Embedded 9004 Series Processors	unaffected EmbGenoaPI-1.0.0.A	Not specified

### References

Reference	Source	Link	Tags
<a href="http://www.amd.com/en/resources/product-security/bulletin/AMD-SB-3016.html">www.amd.com/en/resources/product-security/bulletin/AMD-SB-3016.html</a>	psirt@amd.com	<a href="http://www.amd.com">www.amd.com</a>	
CVE Program record	CVE.ORG	<a href="http://www.cve.org">www.cve.org</a>	canonical
NVD vulnerability detail	NVD	<a href="http://nvd.nist.gov">nvd.nist.gov</a>	canonical, analysis

No vendor comments have been submitted for this CVE.

There are currently no legacy QID mappings associated with this CVE.

© CVE.report 2026 |

Use of this information constitutes acceptance for use in an AS IS condition. There are NO warranties, implied or otherwise, with regard to this information or its use. Any use of this information is at the user's risk. It is the responsibility of user to evaluate the accuracy, completeness or usefulness of any information, opinion, advice or other content. EACH USER WILL BE SOLELY RESPONSIBLE FOR ANY consequences of his or her direct or indirect use of this web site. ALL WARRANTIES OF ANY KIND ARE EXPRESSLY DISCLAIMED. This site will NOT BE LIABLE FOR ANY DIRECT, INDIRECT or any other kind of loss.

CVE, CWE, and OVAL are registered trademarks of [The MITRE Corporation](https://www.mitre.org) and the authoritative source of CVE content is [MITRE's CVE web site](https://www.cve.org). This site includes MITRE data granted under the following [license](#).

Free CVE JSON API [cve.report/api](https://cve.report/api)

CVE.report and Source URL Uptime Status [status.cve.report](https://status.cve.report)